

**Amendments to the Specification:**

Rewrite the paragraph at page 1, line 16 as follows:

A<sub>1</sub> ~~Not applicable. This application claims priority under 35 USC §119(e)(1) of Provisional Application No. 60/248,966, filed November 15, 2000.~~

Rewrite the paragraph at page 2, lines 5 to 12 as follows:

A<sub>2</sub> A multi-core DSP having a shared program memory is described in a co-pending application serial no. \_\_\_\_\_ (Atty. Dkt. #1962-00100) 10/004,492 entitled "Shared Program Memory for use in Multicore DSP Devices" with inventors Kenneth C. Kelly et al., which is hereby incorporated herein by reference. The shared program memory is a volatile memory device that is writeable to permit the software to be changed as desired. However, because the shared program memory is writeable, a processor core in a runaway condition could conceivably corrupt the software, thereby interfering with the operation of other processor cores.